

P2131_A00

GK208 GDDR5 256X32Bit
DVI-I/SlimVGA + HDMI


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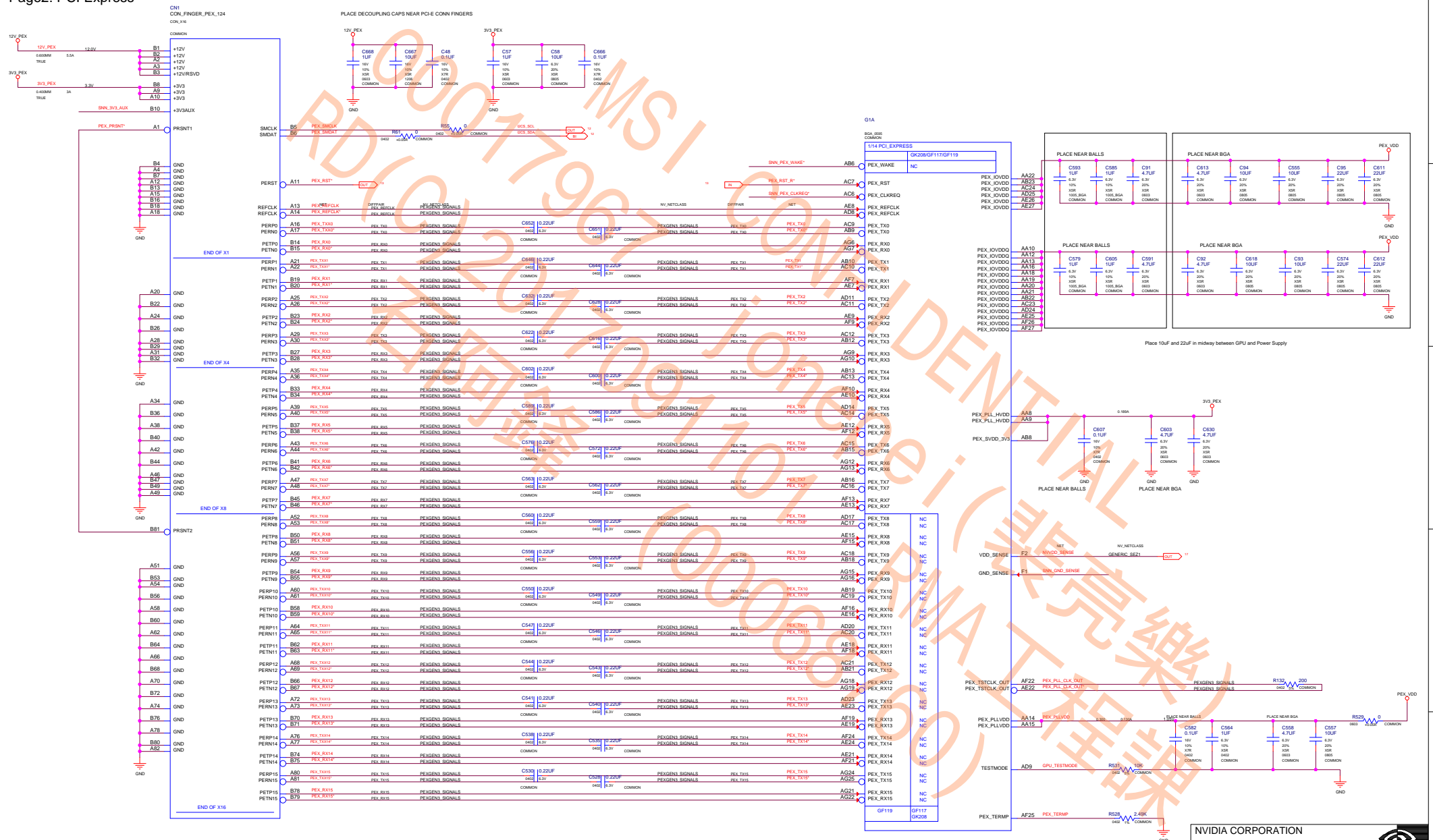
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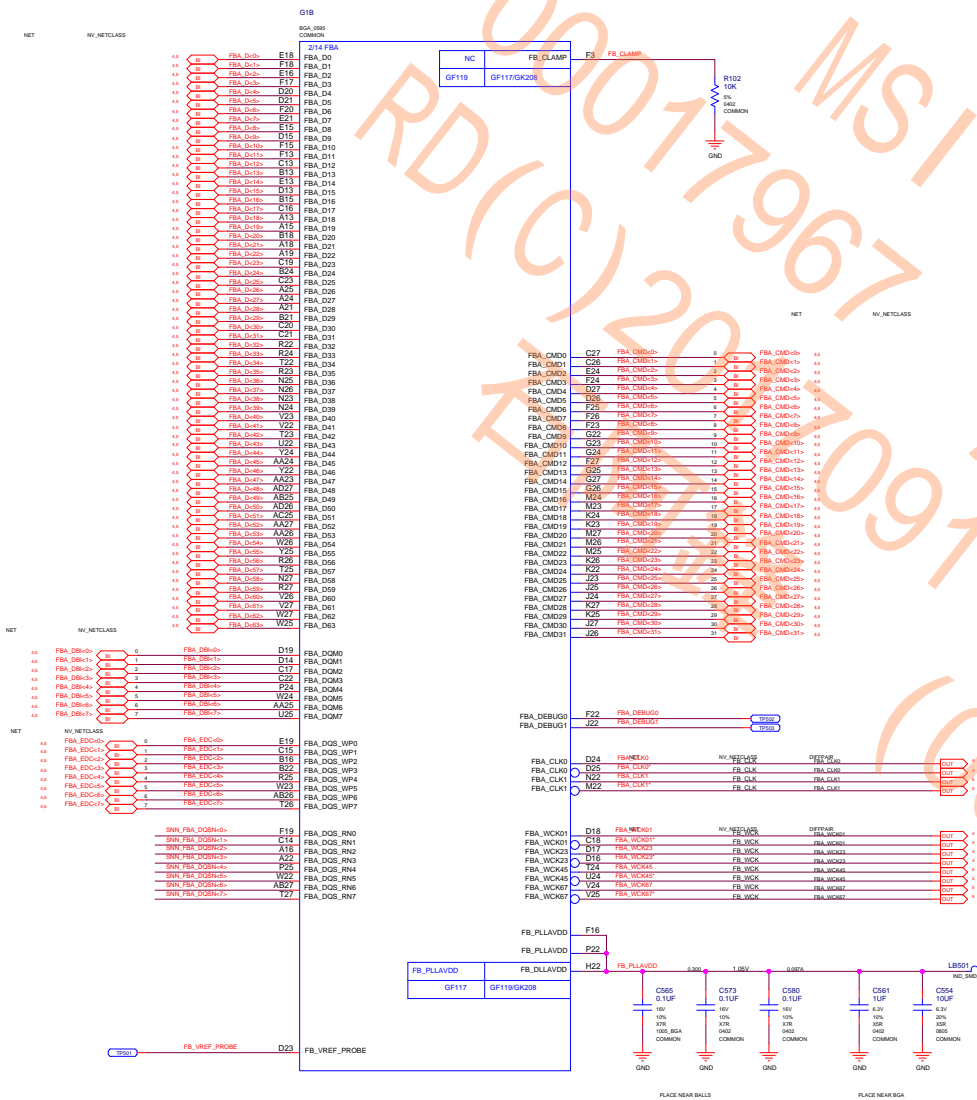
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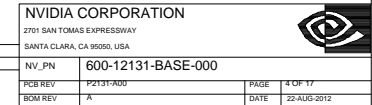
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DECOUPLING FOR M1 and M501 DEVICE

DECOUPLING FOR M2 and M502 DEVICE

PLACE 1uF CAPS CLOSEST TO THE MEMORY DEVICES

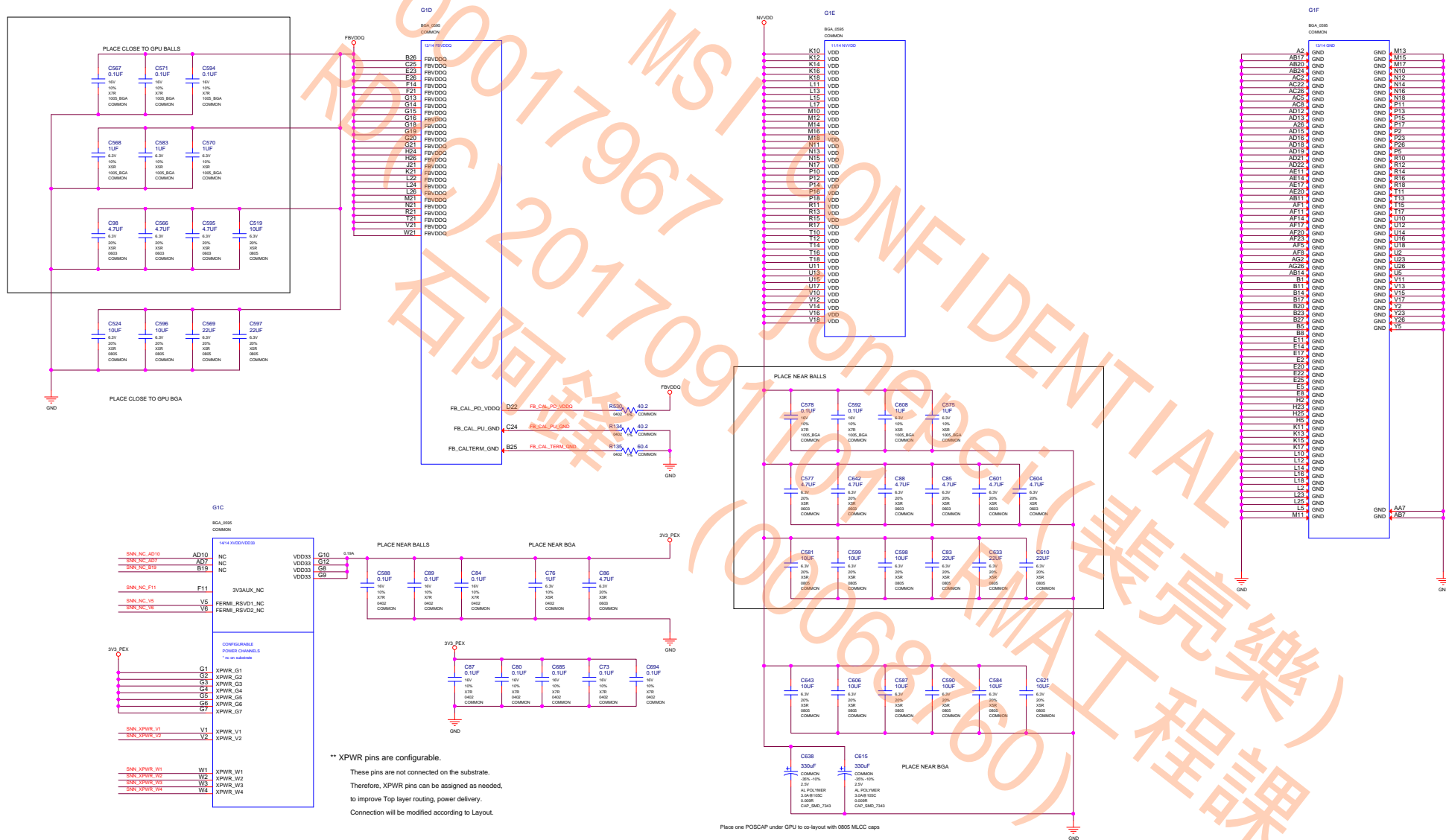
PLACE LARGER CAPACITORS SLIGHTLY FARTHER AWAY

ASSEMBLY	DECOUPLING FOR DRAMs
PAGE DETAIL	

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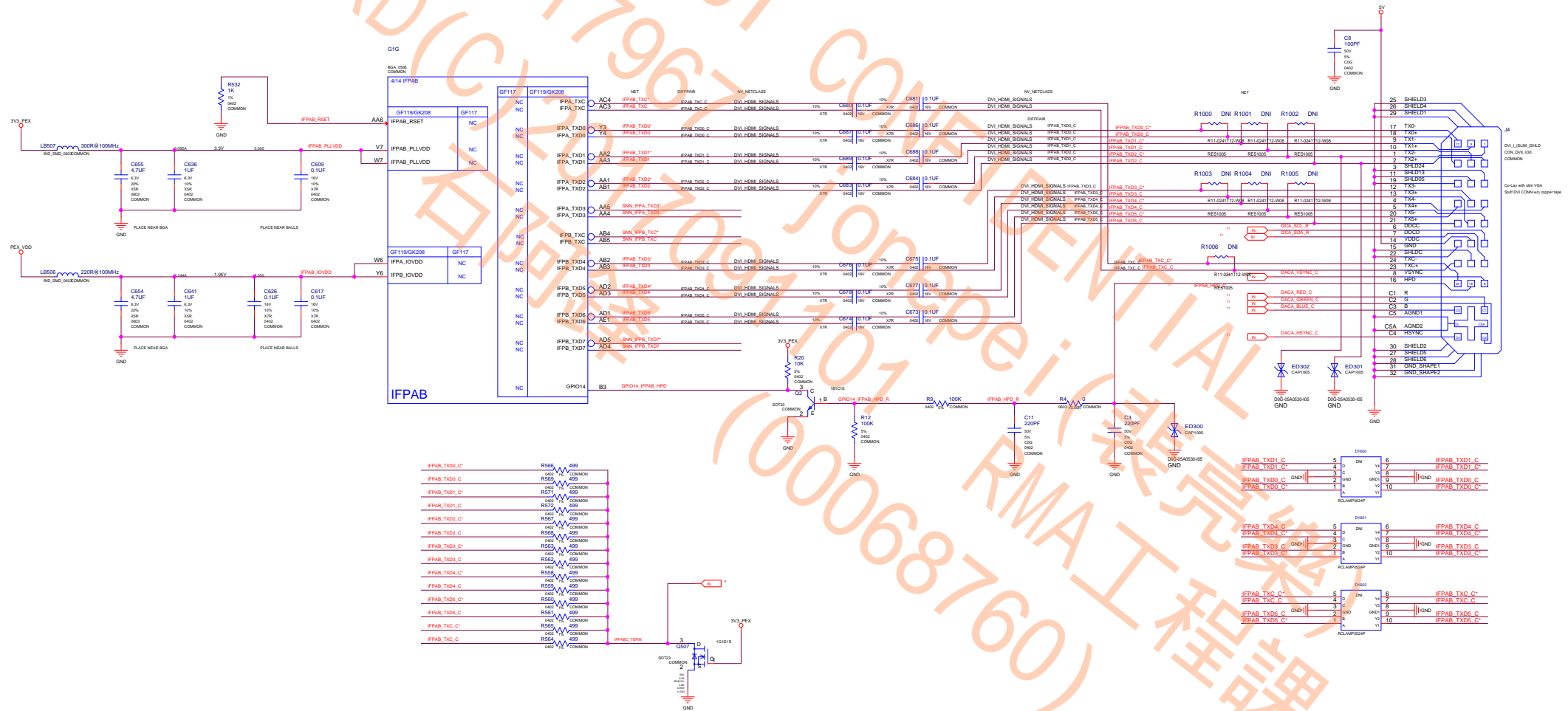


ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	POWER/DECOUPLING: NVVDD, FBVDDQ, VDD33, GND

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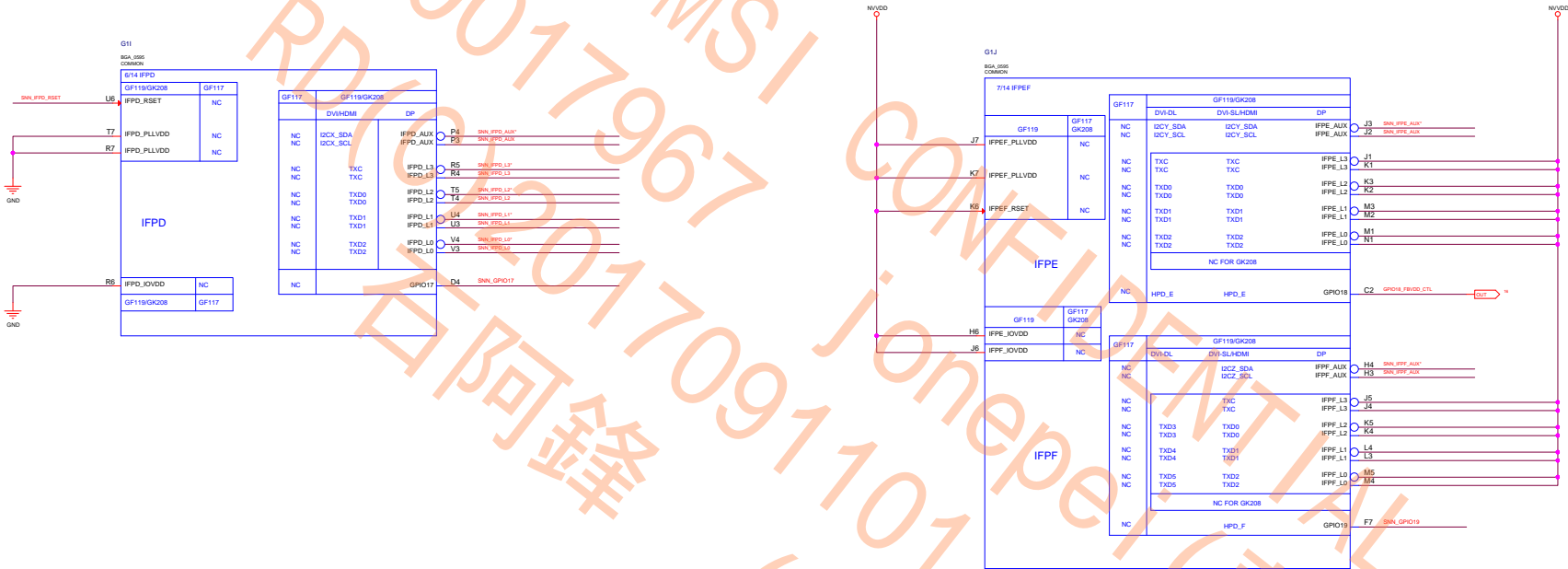
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PAGE DETAIL	UNUSED IFPD, IFPE & IFPF

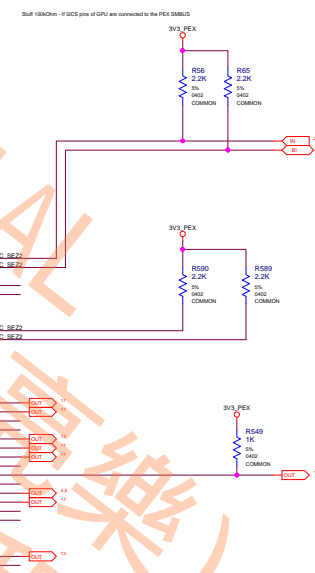
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SOUTH SLIM VGA

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XTALOUTBUFF

XTALSSIN	XTALOUTBUFF	Inverted PWM %
PU	PU	66 (33% HIGH)
PU	PD	50 (50% HIGH)
PD	PU	33 (66% HIGH)
PD	PD	0 (100% HIGH)



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Multilevel Straps

0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC

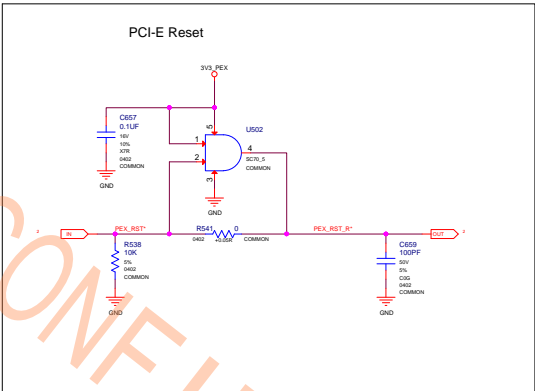
GK208 Straps

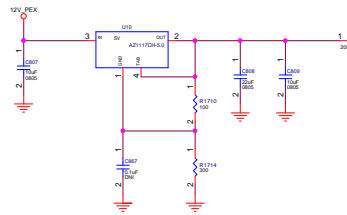
Bit Signal

Values

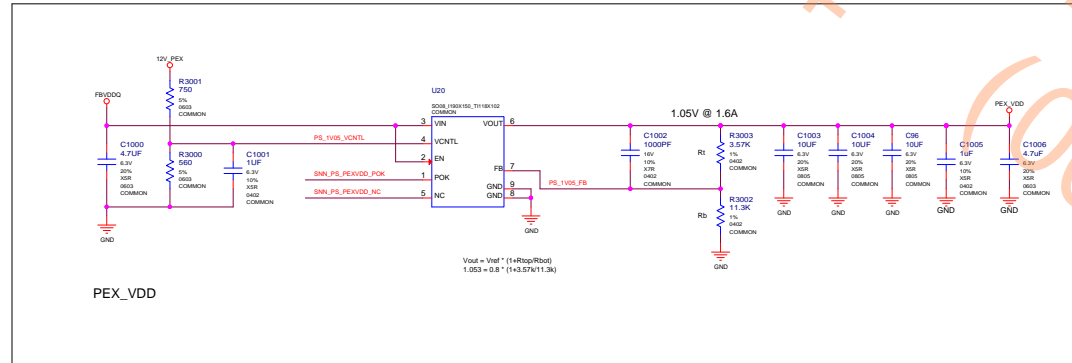
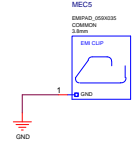
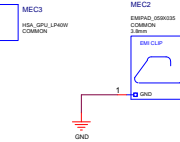
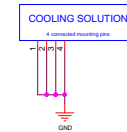
03	RAMCFG0	0000	Reserved			ROM_SI
02	RAMCFG0	0001	1GBx16 GDDR5 X16 64GB 64GB			
01	RAMCFG0	0010	1GBx16 GDDR5 X16 64GB 64GB			
00	RAMCFG0	0011	1GBx16 GDDR5 X16 64GB 64GB			
03	FBP1	01	128MB			ROM_SO
02	FBP1	10	256MB			
01	SMB_ALT_ADDR	0	DISABLED	1	ENABLED	ROM_SCLK
00	VGA_DEVICE	0	DISABLED	1	ENABLED	
03	PCI_DEV0_EXT	0000	0	SKU1	TBD	
02	SUB_VENDOR	0	NO BIOS	1	BIOS	
01	PCI_DEV0_EXT_BTS	0000	0	SKU1	TBD	
00	PEX_FULL_EN_TERMINO	0	DISABLED	1	ENABLED	
03	USER0	0000	NOT			Strap0
02	USER0					
01	USER0					
00	USER0					
03	3G0_PADCFG_LUT_ADDR0	0000	DISKTOP_DEFAULT	1000	DISKTOP_JTTMS	Strap1
02	3G0_PADCFG_LUT_ADDR0	0001	MOBILE_DEFAULT	1001	MOBILE_JTTMS	
01	3G0_PADCFG_LUT_ADDR0	0010	MOBILE_JTTMS_LAMP	1010	MOBILE_JTTMS_LAMP	
00	3G0_PADCFG_LUT_ADDR0	0011	MOBILE_JTTMS_LAMP	1011	MOBILE_JTTMS_LAMP	
03	3G0_PADCFG_LUT_ADDR0	0100	MOBILE_JTTMS_LAMP	1100	MOBILE_JTTMS_LAMP	
02	3G0_PADCFG_LUT_ADDR0	0101	MOBILE_JTTMS_LAMP	1101	MOBILE_JTTMS_LAMP	
01	3G0_PADCFG_LUT_ADDR0	0110	MOBILE_JTTMS_LAMP	1110	MOBILE_JTTMS_LAMP	
00	3G0_PADCFG_LUT_ADDR0	0111	MOBILE_JTTMS_LAMP	1111	MOBILE_JTTMS_LAMP	
03	PCI_DEV000	0000	0010			Strap2
02	PCI_DEV000					
01	PCI_DEV000	0010	TBD			
00	PCI_DEV000					
03	SC0L_EXP000	0	DISABLED	1	ENABLED	Strap3
02	SC0L_EXP000	1	ENABLED			
01	SC0L_EXP000	0	DISABLED	1	ENABLED	
00	SC0L_EXP000	0	DISABLED	1	ENABLED	
03	PCI_SPEED_CHANGE_GEN0	0	DISABLED	1	ENABLED	Strap4
02	PCI_SPEED_CHANGE_GEN0	1	GEN1			
01	PCI_SPEED_CHANGE_GEN0	0	GEN2			
00	DP_FULL_VDD_3V3	0	USE V0			

PCI-E Reset

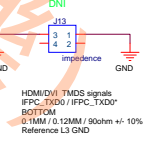
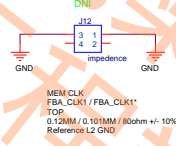
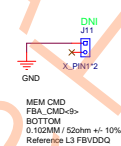
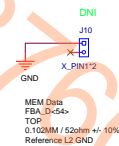




Mechanical



Reserve J10 - J13 for Impedance check



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PAGE DETAIL	PS: 5V, PEXVDD

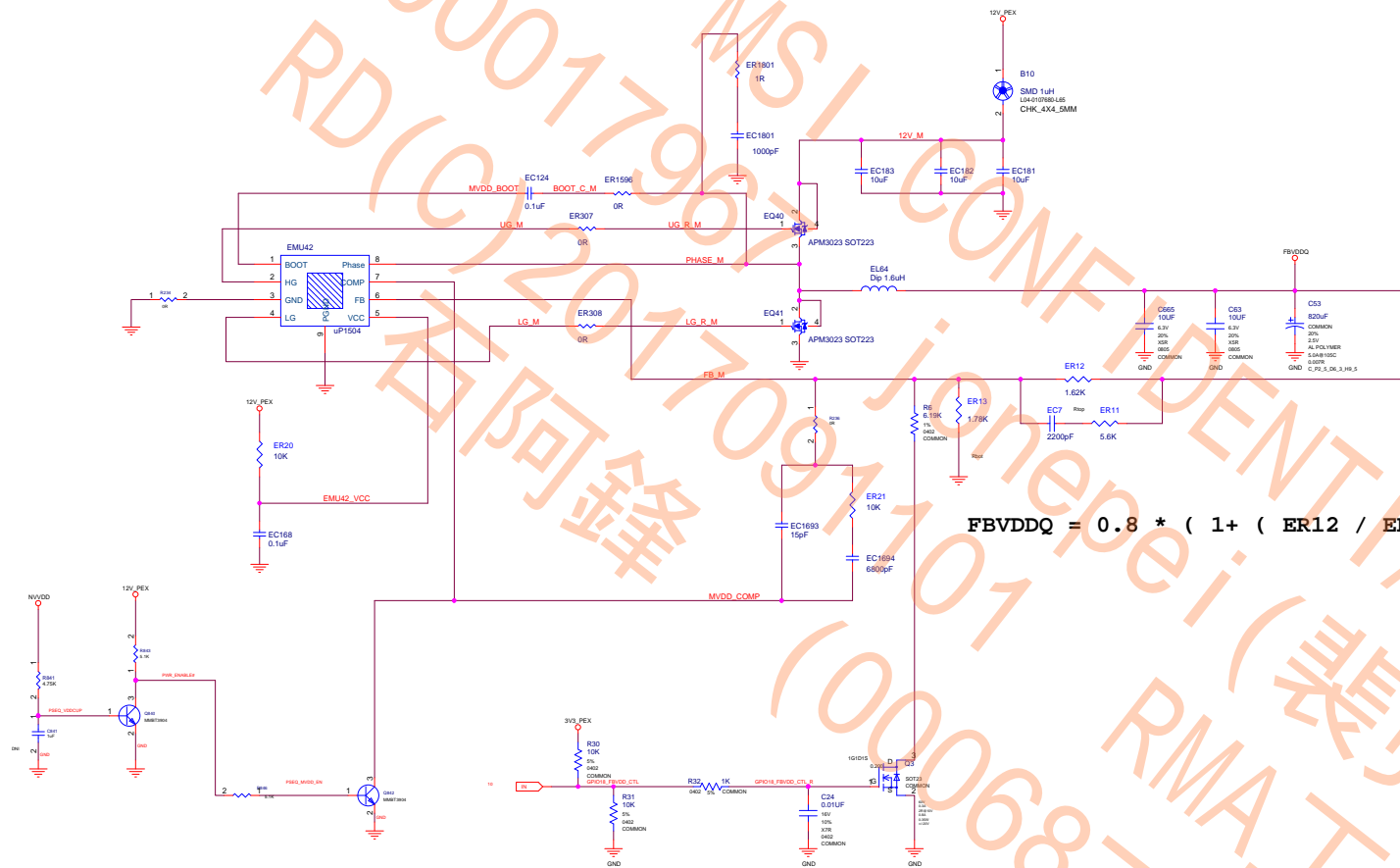
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$$FBVDDQ = 0.8 * (1 + (ER12 / ER13))$$

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PAGE DETAIL	PS FBVDDQ

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